

Product data sheet

### 1. General description

High power density, hyperfast PN-rectifier with high-efficiency planar technology, encapsulated in a small and flat lead SOD123W Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Reverse voltage V<sub>R</sub> ≤ 200 V
- Forward current  $I_F \le 1 A$
- Switching time  $t_{rr} \le 25$  ns
- Pt doped life time control
- Low inductance
- Small and flat lead SMD plastic package
- Package height typ. 1 mm
- High power capability due to clip-bond technology
- Planar die design
- Capable for reflow and wave soldering

### 3. Applications

- General-purpose rectification
- Reverse polarity protection
- Hyperfast switching
- Freewheeling applications

### 4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 140 °C		-	-	1	A
V <sub>RRM</sub>	repetitive peak reverse voltage	T <sub>j</sub> = 25 °C		-	-	200	V
V <sub>R</sub>	reverse voltage	-		-	-	200	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	845	930	mV
		I <sub>F</sub> = 1 A; pulsed; T <sub>j</sub> = 125 °C	[1]	-	700	790	mV
I <sub>R</sub>	reverse current	$V_R$ = 200 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	5	200	nA
		V <sub>R</sub> = 200 V; pulsed; T <sub>i</sub> = 125 °C	[1]	-	1.5	20	μA

[1] Very short pulse, in order to maintain a stable junction temperature.

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### 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	K	cathode					
2	A	anode					
			CFP3 (SOD123W)	006aab040			

### 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
ES1DR	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
ES1DR	КМ

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>RRM</sub>	repetitive peak reverse voltage	T <sub>j</sub> = 25 °C		-	200	V
V <sub>R</sub>	reverse voltage			-	200	V
V <sub>RMS</sub>	RMS voltage			-	140	V
l <sub>F</sub>	forward current	δ = 1; T <sub>sp</sub> ≤ 137 °C		-	1.4	А
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 140 °C		-	1	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8.3 ms; $T_{j(init)}$ = 25 °C; single half sine wave (applied at rated load condition)		-	32	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	735	mW
			[2]	-	1.19	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance	in free air	[1]	-	-	170	K/W
	from junction to ambient		[2]	-	-	105	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[3]	-	-	15	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

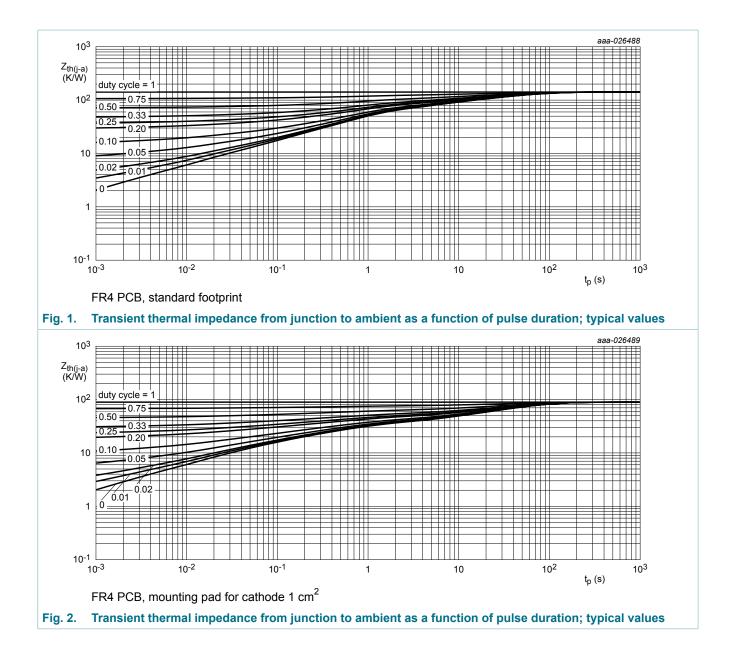
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[3] Soldering point of cathode tab.



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ES1DR



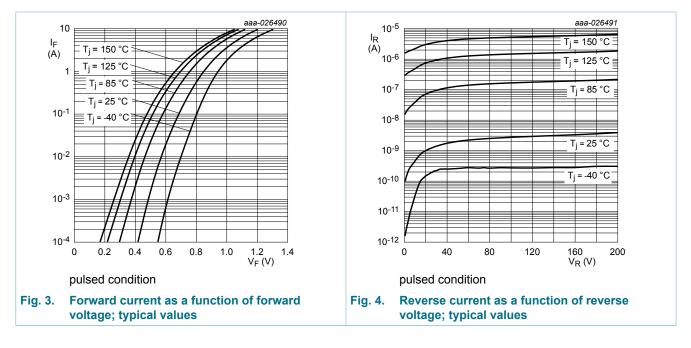
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### **10. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	$I_R$ = 100 µA; pulsed; $T_j$ = 25 °C	[1]	200	-	-	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	845	930	mV
		I <sub>F</sub> = 1 A; pulsed; T <sub>j</sub> = 125 °C	[1]	-	700	790	mV
I <sub>R</sub>	reverse current	$V_R$ = 200 V; pulsed; $T_j$ = 25 °C	[1]	-	5	200	nA
		$V_R$ = 200 V; pulsed; $T_j$ = 125 °C	[1]	-	1.5	20	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	17	-	pF
t <sub>rr</sub>	reverse recovery time ; step recovery	$I_F$ = 0.5 A; $I_R$ = 1 A; $I_{R(meas)}$ = 0.25 A; $T_j$ = 25 °C		-	10	25	ns
	reverse recovery time ; ramp recovery	$I_F = 1 \text{ A}; \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}; \text{ V}_R = 30 \text{ V}; \\ T_j = 25 ^\circ\text{C}$		-	20	-	ns
V <sub>FRM</sub>	peak forward recovery voltage	I <sub>F</sub> = 1 A; dI <sub>F</sub> /dt = 50 A/μs; T <sub>j</sub> = 25 °C		-	930	-	mV

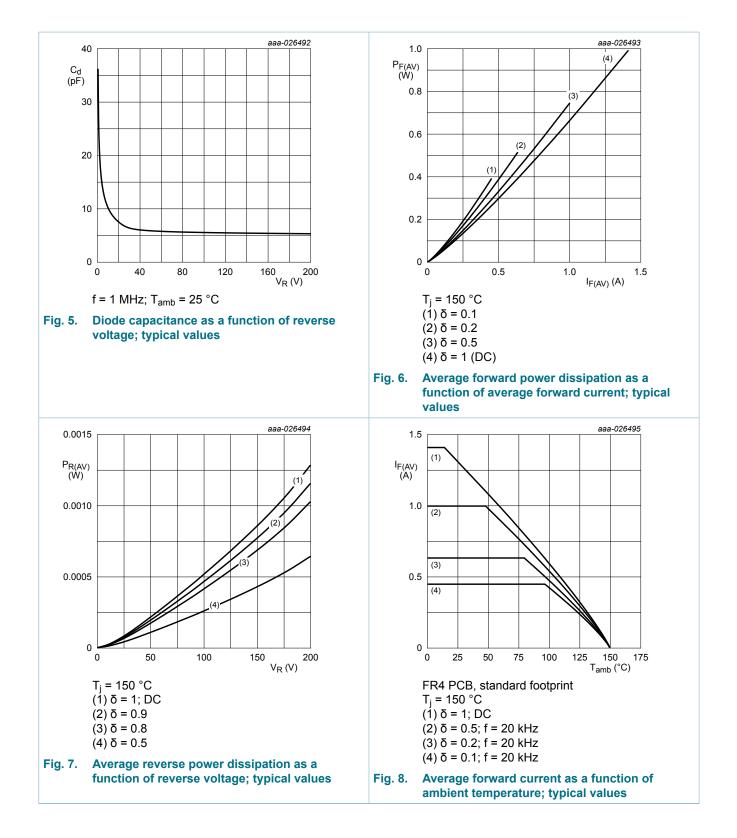
[1] Very short pulse, in order to maintain a stable junction temperature.



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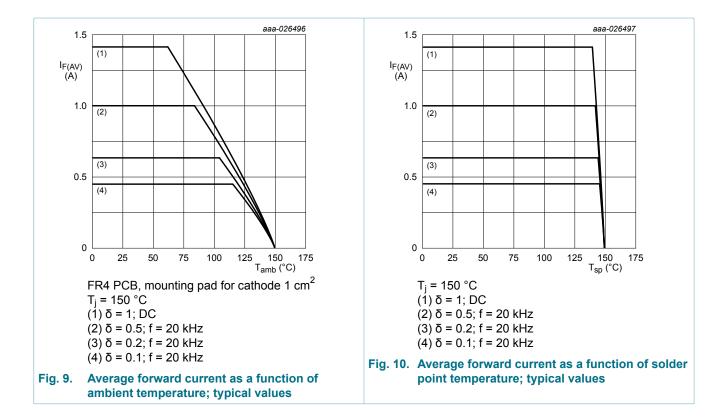


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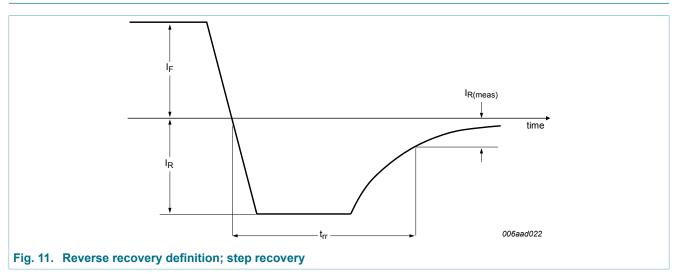
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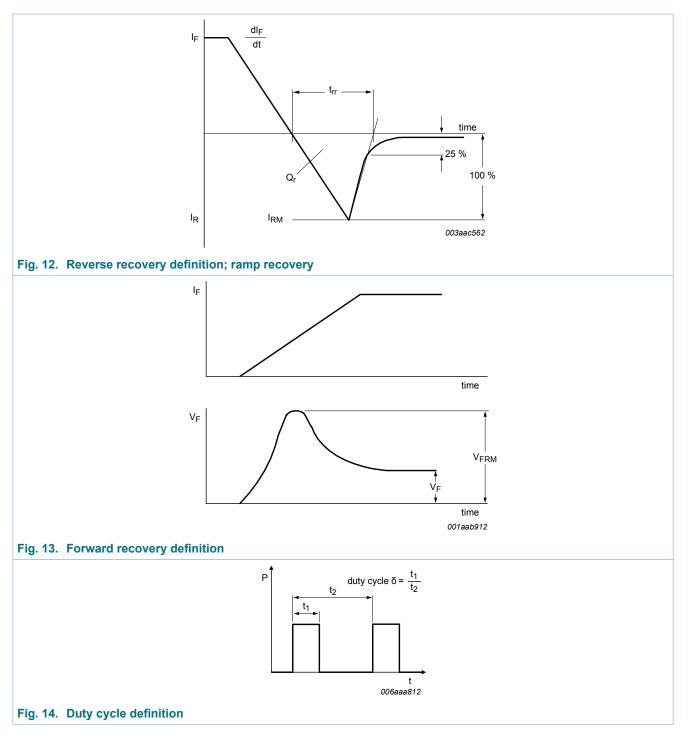
#### 200 V, 1 A hyperfast PN-rectifier



### 11. Test information



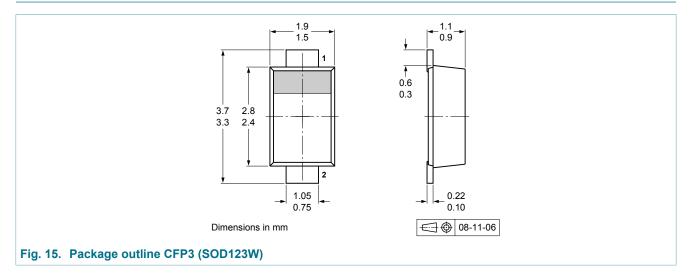
#### 200 V, 1 A hyperfast PN-rectifier



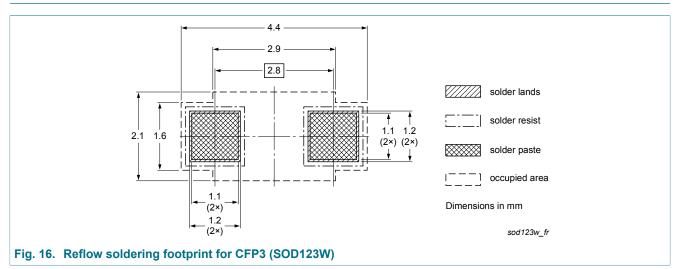
The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

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### 12. Package outline



### 13. Soldering



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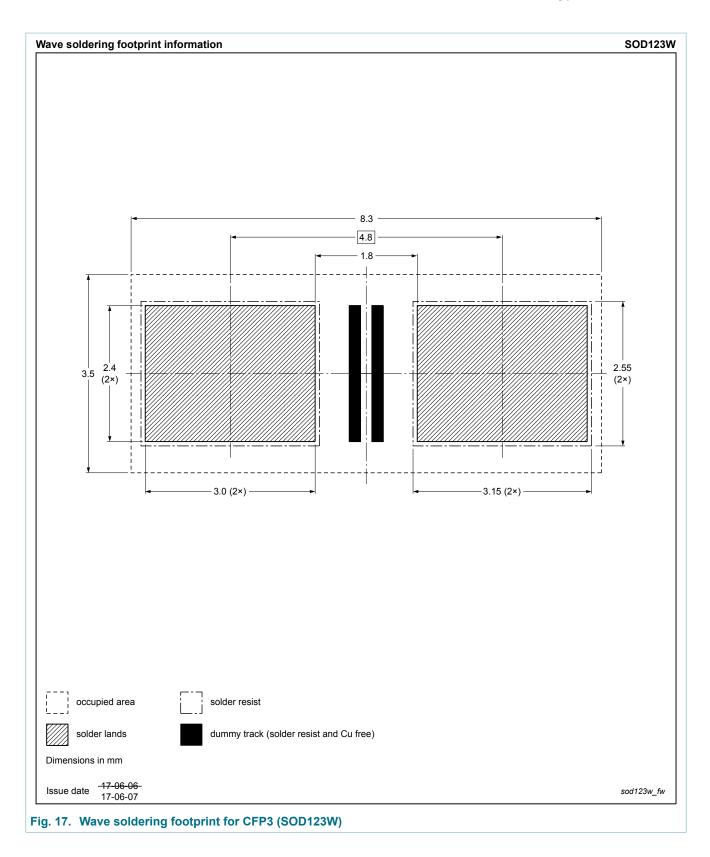
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## **ES1DR**

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# 14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
ES1DR v.2	20180328 Product data sheet - ES1DR v.1						
Modifications:	<ul> <li>Features and benefits: Capable for reflow and wave soldering added</li> <li>Soldering: Wave soldering footprint added</li> </ul>						
ES1DR v.1 20170331 Product data sheet							

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### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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